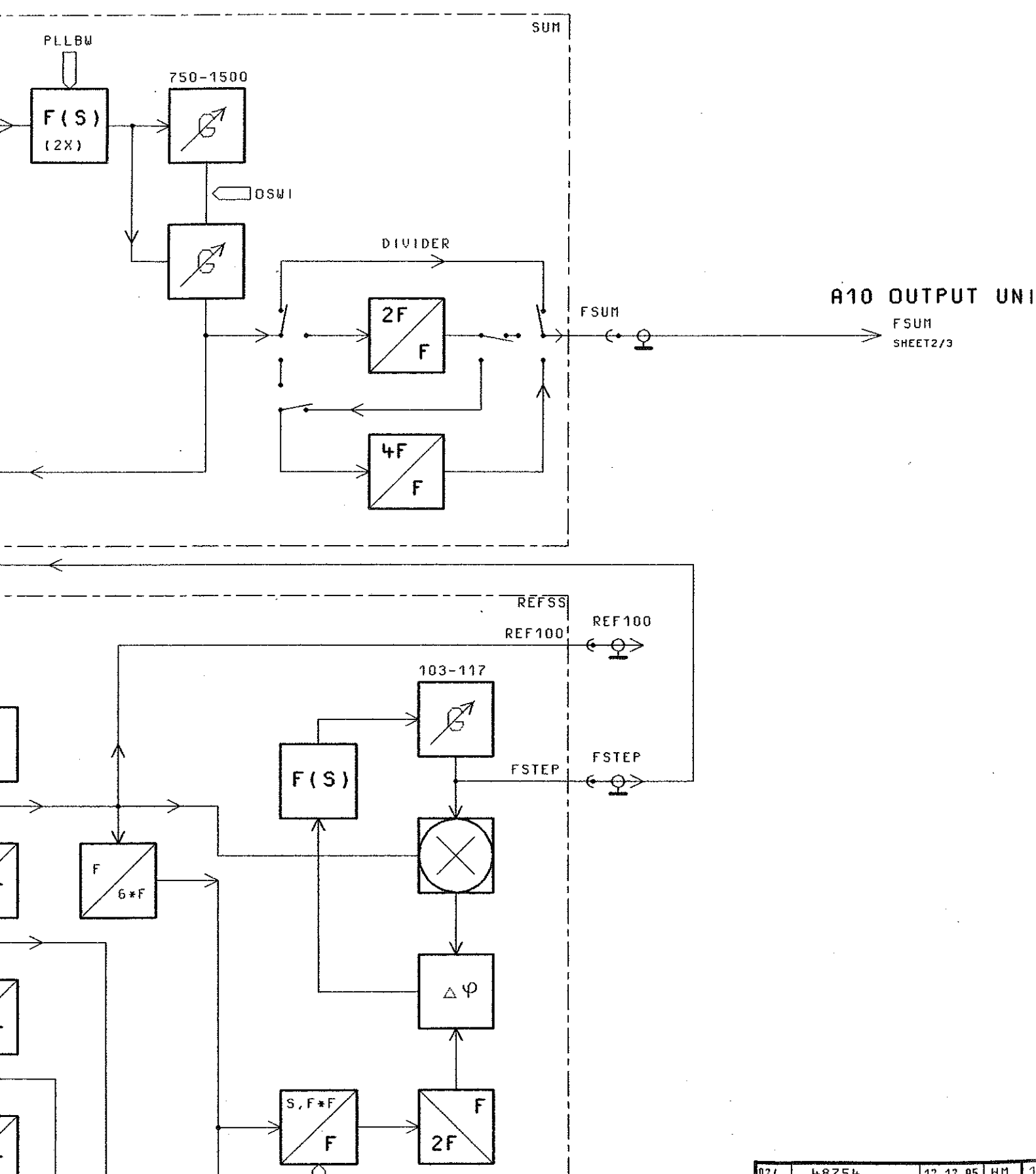
A10 OUTPUT UNIT

FSUM
SHEET2/3

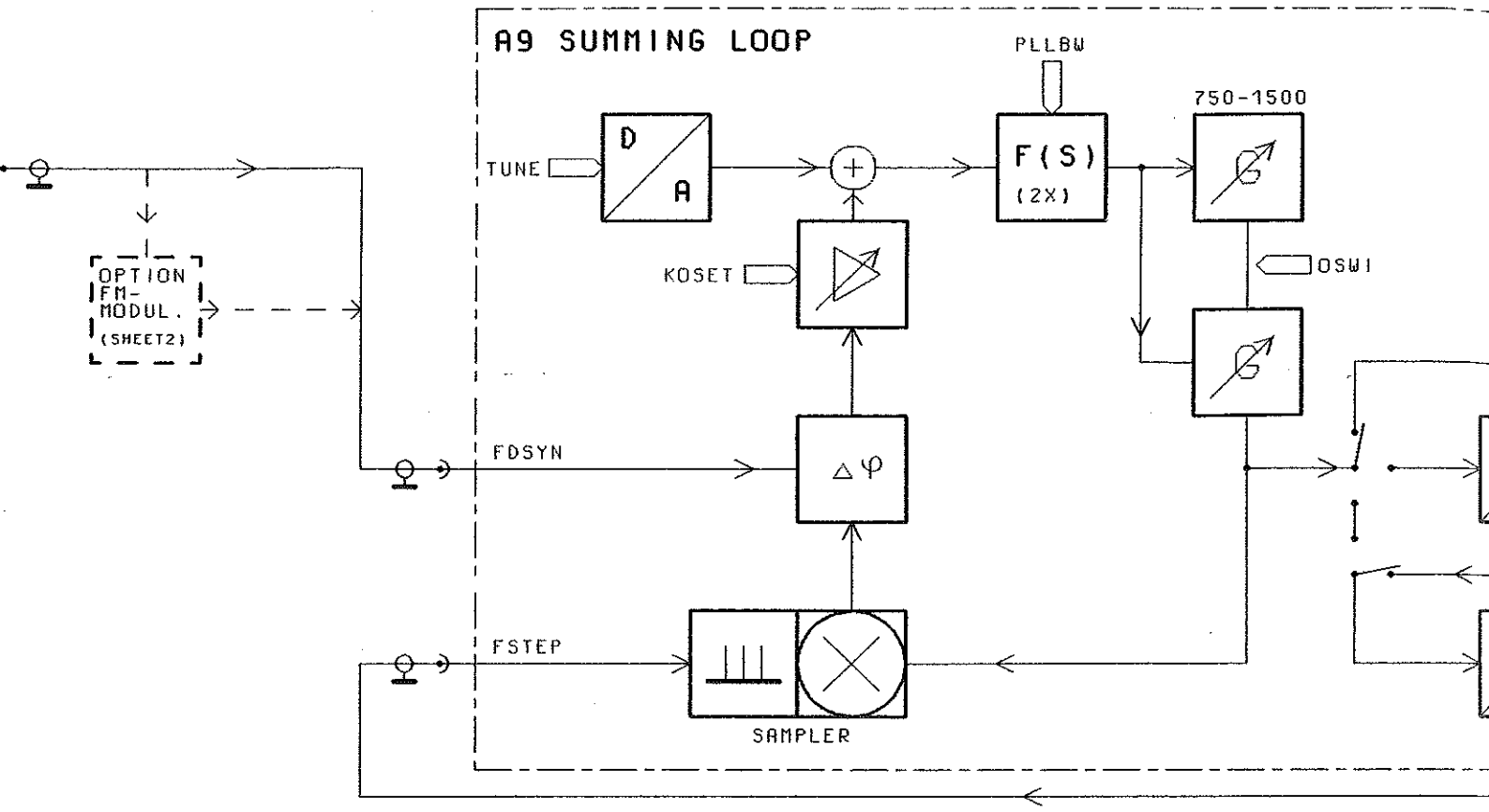
VAR02/03/22/23/24

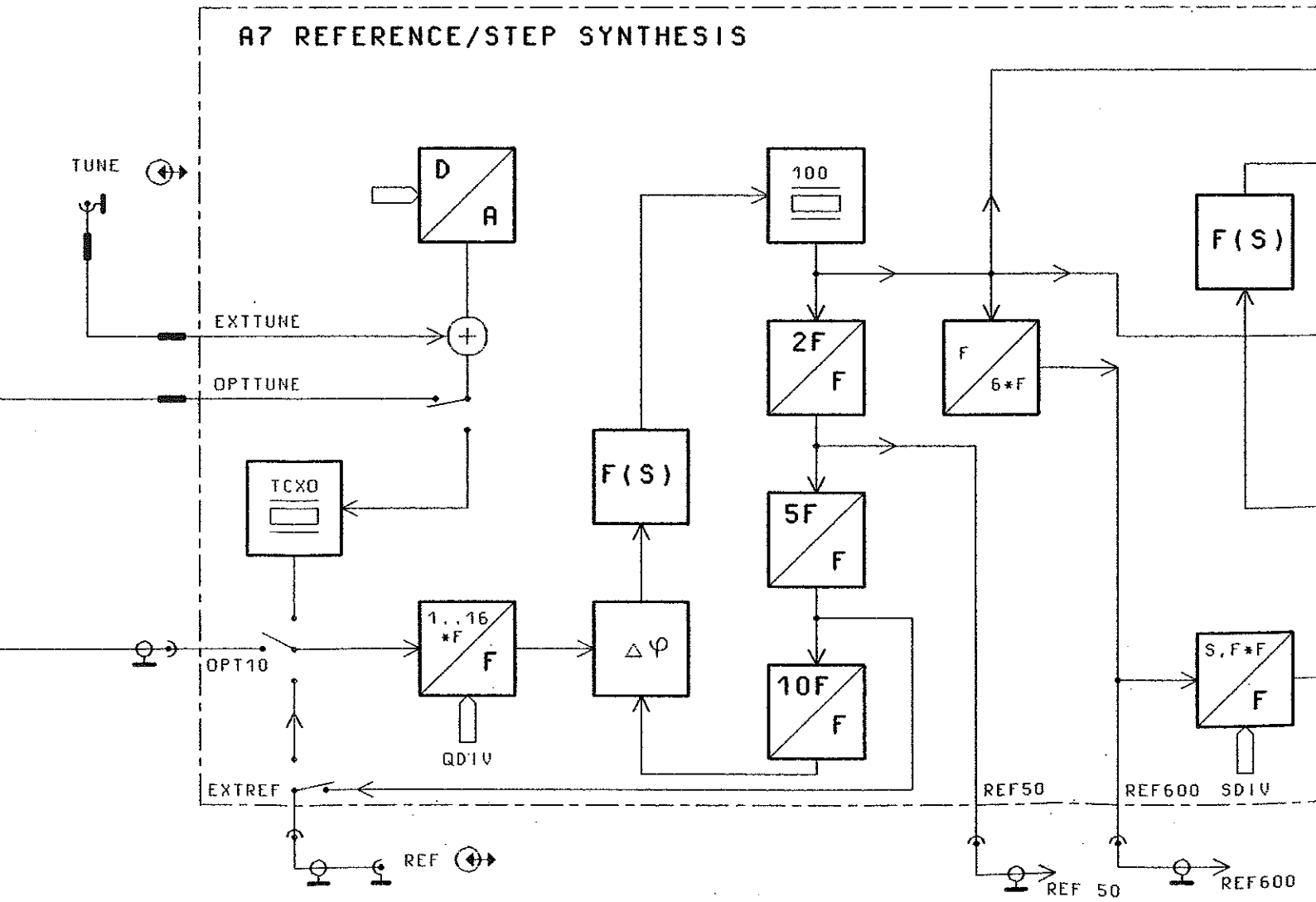
02/	48754	12.12.95	HM	1GPK	TAG	NAME	BENENNUNG
				BEARB.		HM	SME SIGNALGENERATOR
				GEPR.			SME SIGNAL GENERATOR
				NORM			
				PLOTT	23.02.96		
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1038.6002.01 FS
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SME		REG.I.V. 1038.6002 ERSTE Z.

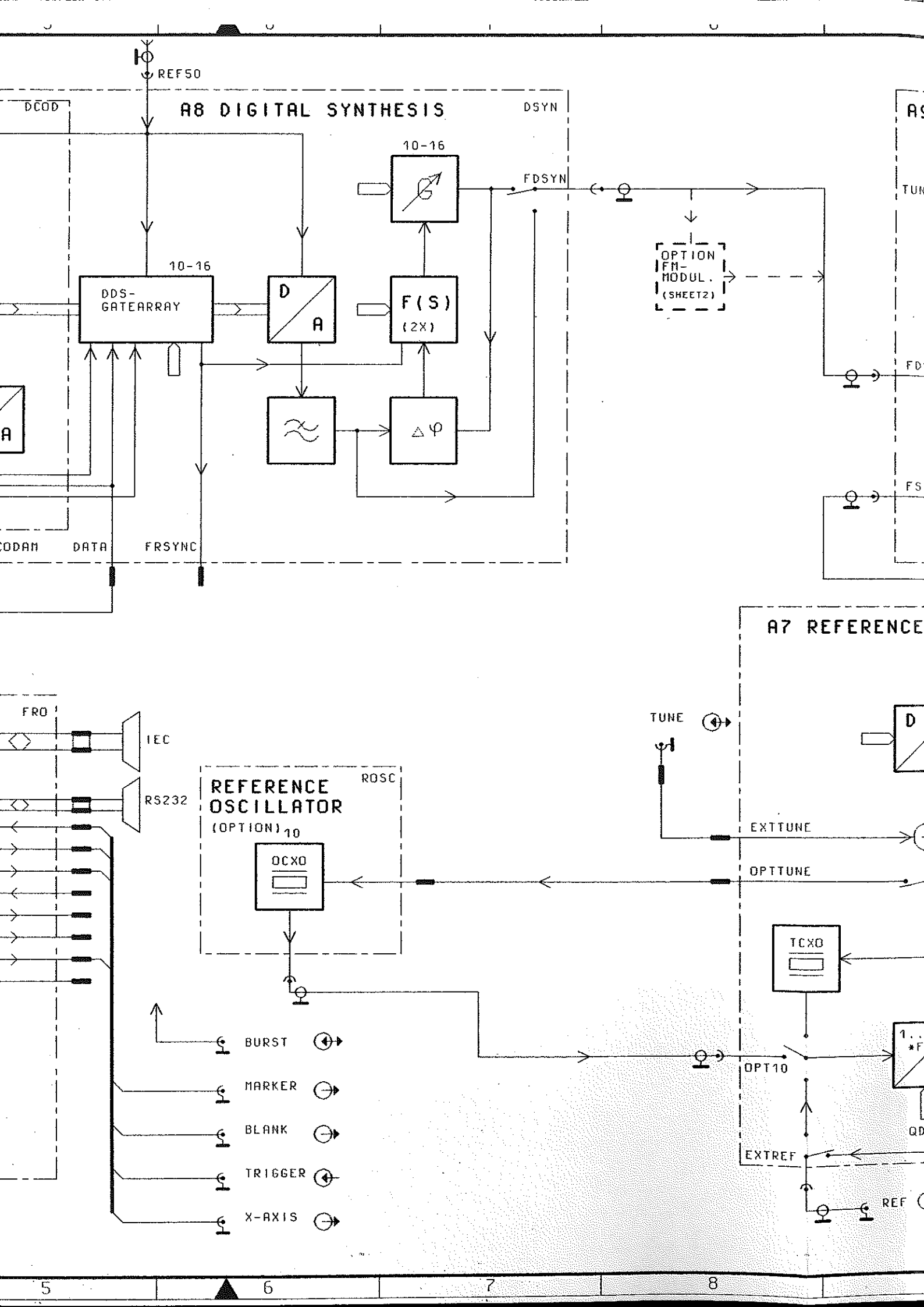
BLATT-NR. 1+ V. BL.

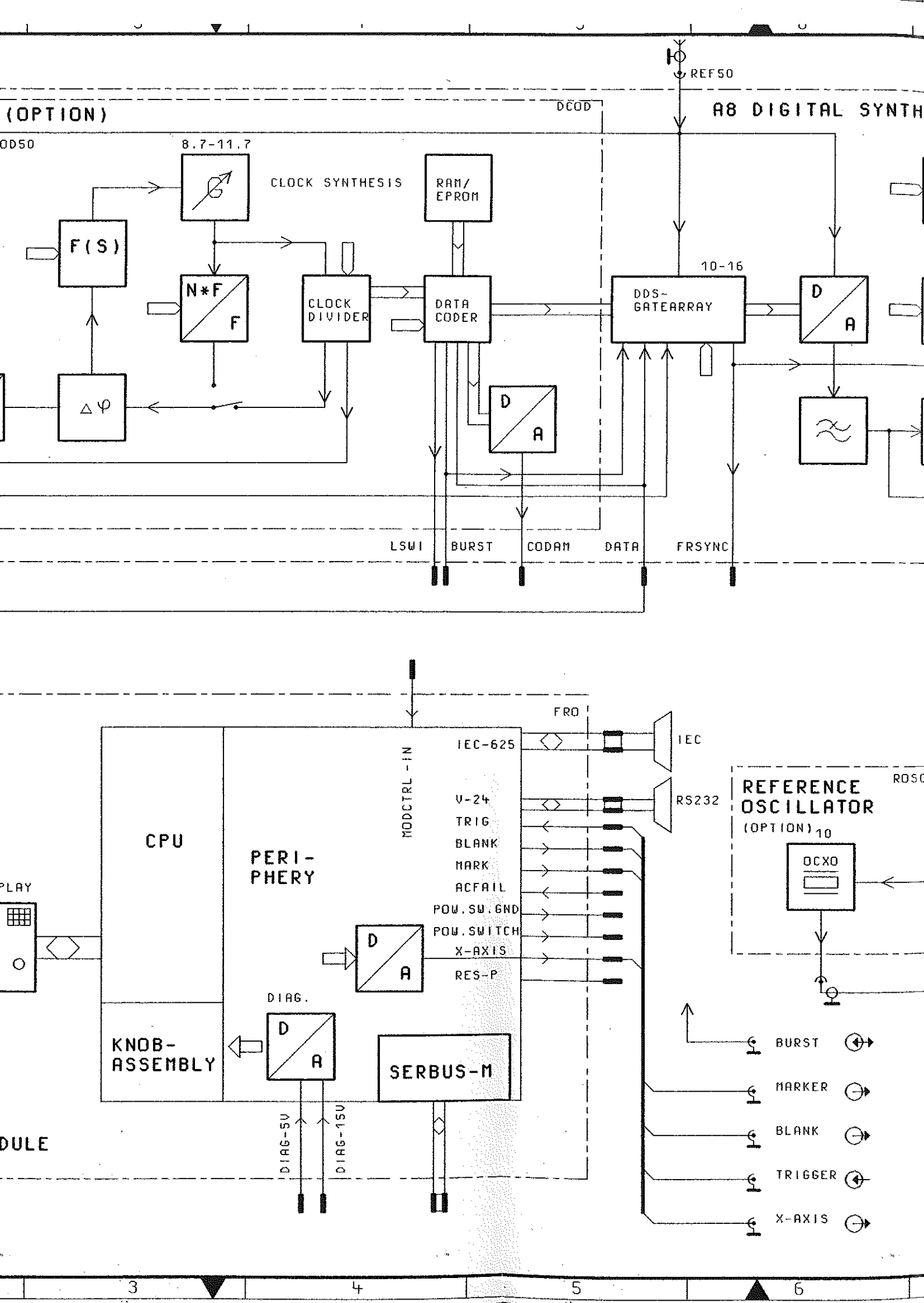


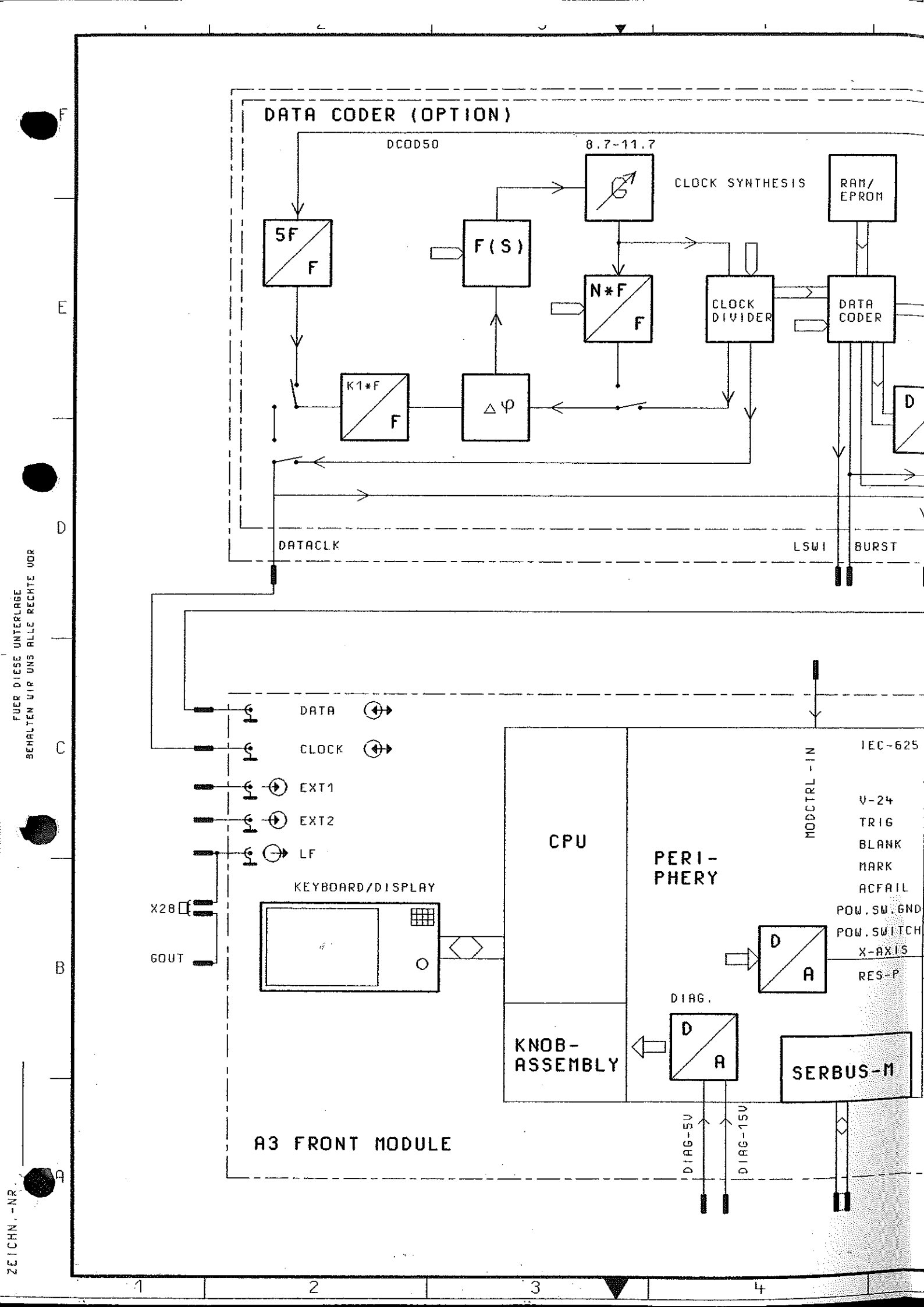
DZ/	48754	12.12.95	HM
/			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME


A9 SUMMING LOOP
PLLBW
750-1500
TUNE
D
A
F(S)
(2X)
OPTION FM-MODUL. (SHEET2)
KOSET
OSWI
FDSYN
Δφ
FSTEP
SAMPLER


A7 REFERENCE/STEP SYNTHESIS
TUNE
D
A
100
F(S)
EXTTUNE
2F
F
F
6*F
OPTTUNE
TCXO
F(S)
5F
F
1..16
*F
Δφ
OPT10
10F
S,F*F
QDIV
SDIV
EXTREF
REF50
REF600
REF
REF 50
REF600

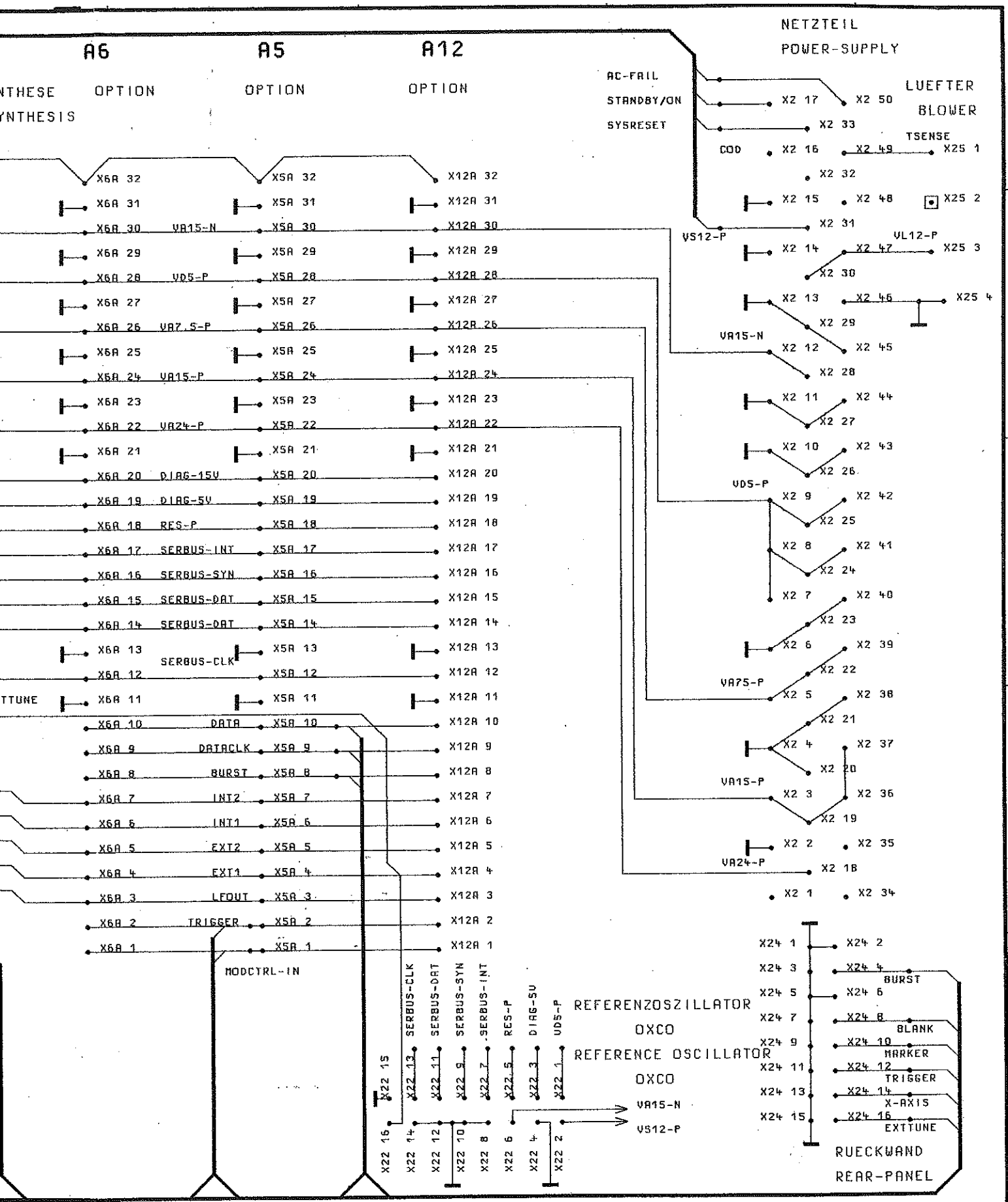

REF50
A8 DIGITAL SYNTHESIS
DCOD
DSYN
10-16
FDSYN
OPTION FM-MODUL. (SHEET2)
10-16
DDS-GATEARRAY
D A
F(S) (2X)
ΔφA
CODAM
DATA
FRSYNC
A7 REFERENCE
FRO
IEC
RS232
REFERENCE OSCILLATOR (OPTION)10
ROSC
OCXO
TUNE
EXTTUNE
OPTTUNE
TCXO
BURST
MARKER
BLANK
TRIGGER
X-AXIS
OPT10
EXTREF
REF


REF50
(OPTION)
DCOD
A8 DIGITAL SYNTH
OD50
8.7-11.7
CLOCK SYNTHESIS
RAM/
EPROM
F(S)
N*F
F
CLOCK
DIVIDER
DATA
CODER
10-16
DDS-
GATEARRAY
D
A
△φ
LSWI
BURST
CODAM
DATA
FRSYNC
FRO
IEC-625
IEC
V-24
RS232
TRIG
BLANK
MARK
ACFAIL
POW.SW.GND
POW.SWITCH
X-AXIS
RES-P
MODCTRL-IN
CPU
PERI-
PHERY
REFERENCE
OSCILLATOR
(OPTION) 10
OCXO
ROSC
PLAY
DIAG.
KNOB-
ASSEMBLY
SERBUS-M
DIAG-5V
DIAG-15V
DULE
BURST
MARKER
BLANK
TRIGGER
X-AXIS



FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

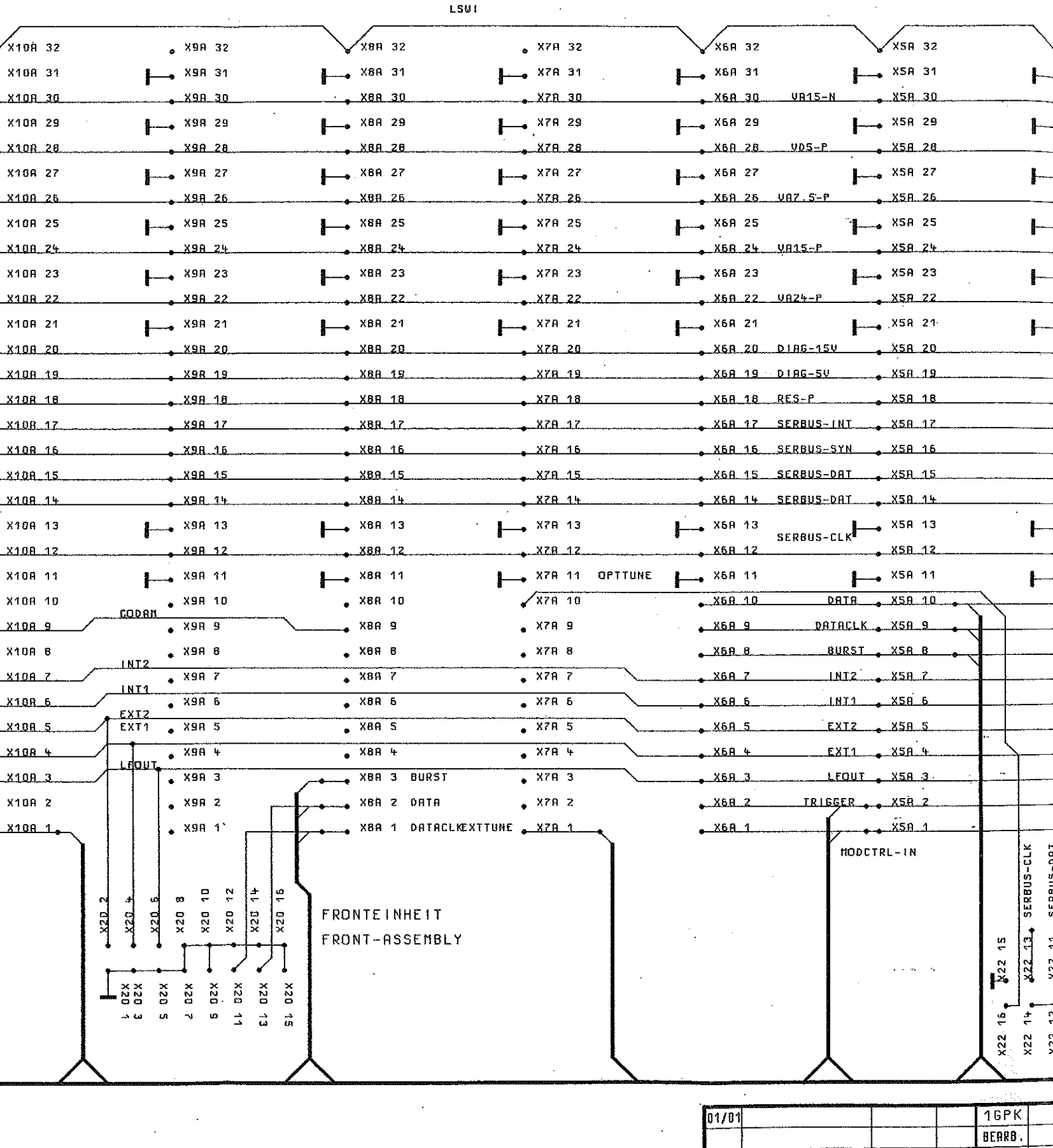
ZEICHN.-NR.



01/01				1GPK	TAG	NAME	BENENNUNG
				BEARB.		JN	MOTHERBOARD
				GEPR.		JN	MOTHERBOARD
				NORM			
				PLOTT	26.01.94		
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1038.6760.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SME		REG.I.V. 1038.6002

BLATT-NR. 1-
V. BL.
ERSTE Z.

A9	A8	A7	A6	A5	A
L 1,5GHZ SUMMIERSCHLEIFE	DIGITALE SYNTHESE	REFERENZ/STEPSYNTHESE	OPTION	OPTION	OP
IT 1,5GHZ SUMMING LOOP	DIGITAL SYNTHESIS	REFERENCE/STEPSYNTHESIS			

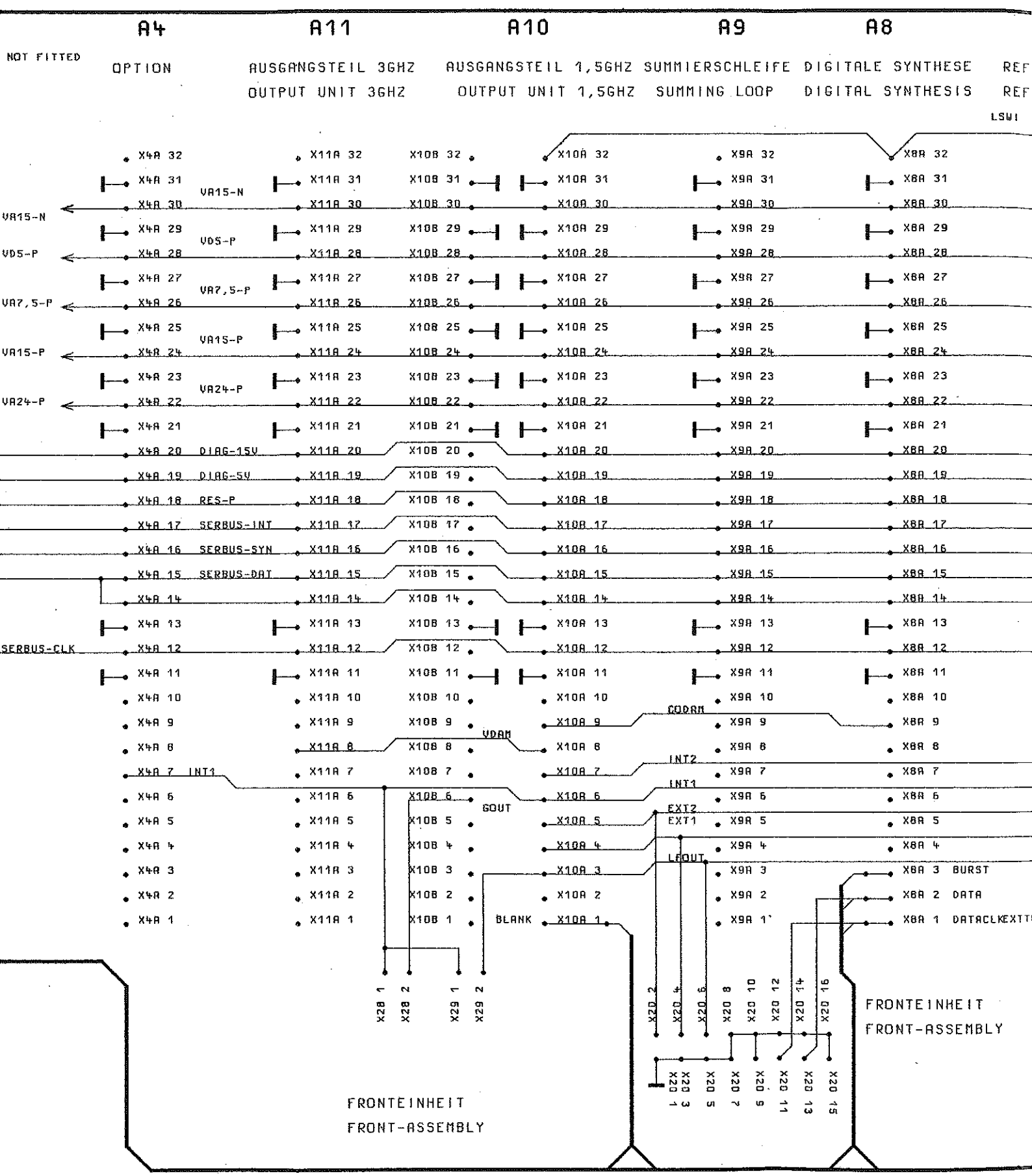


ER VAR.02

TOR MOD.02

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

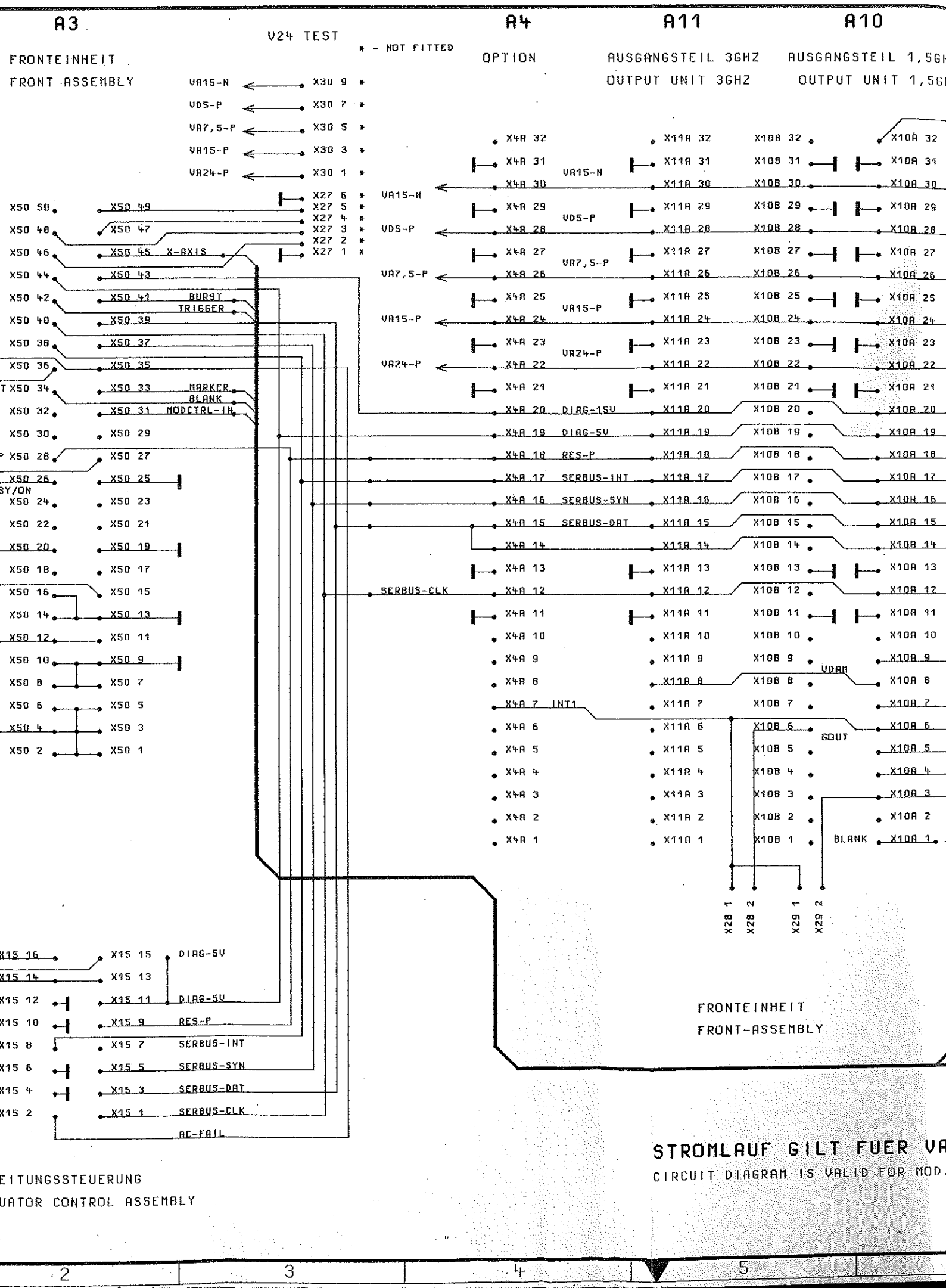
01/01				1GPK	
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	26
/				R/S ROHDE	
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	

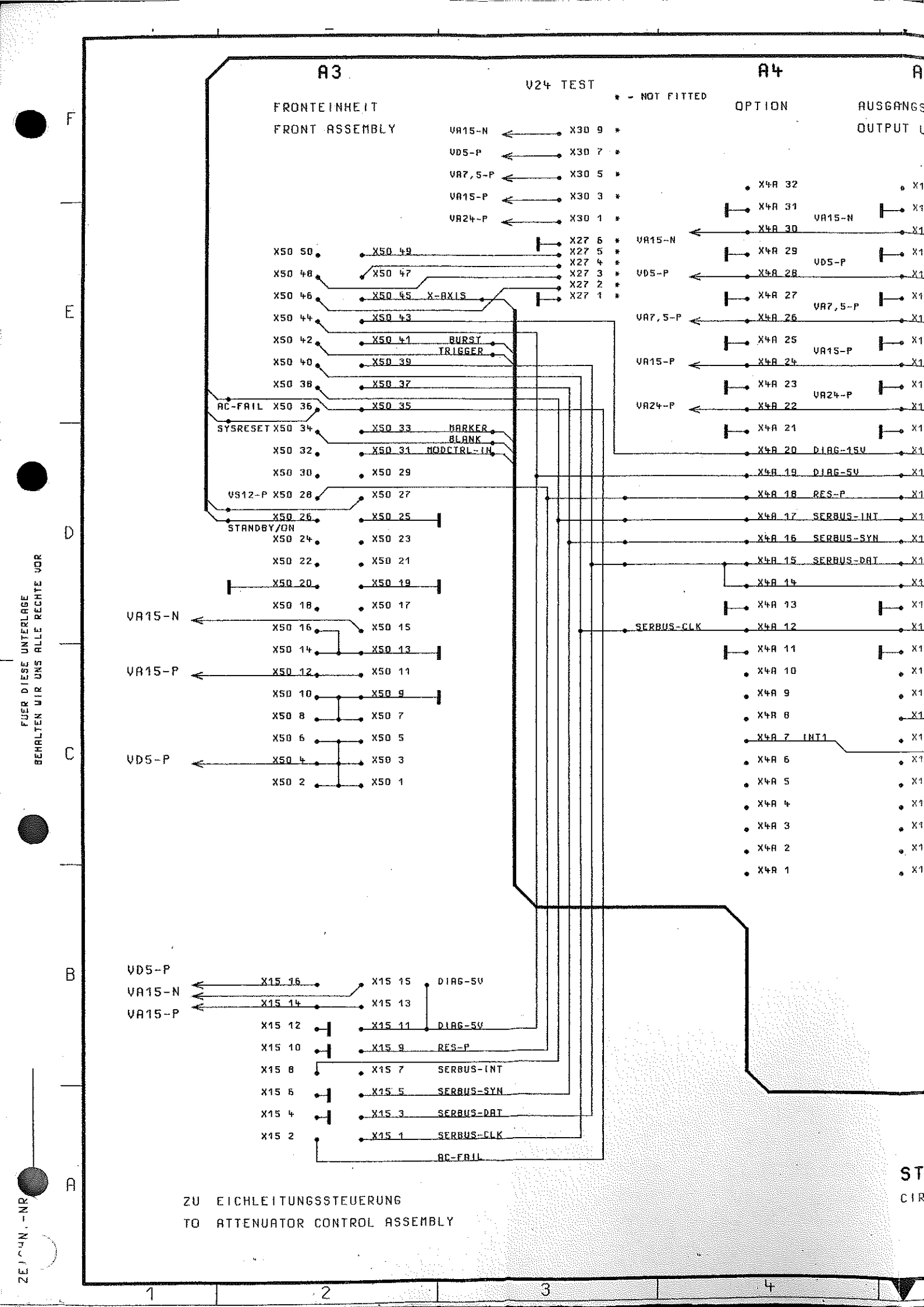


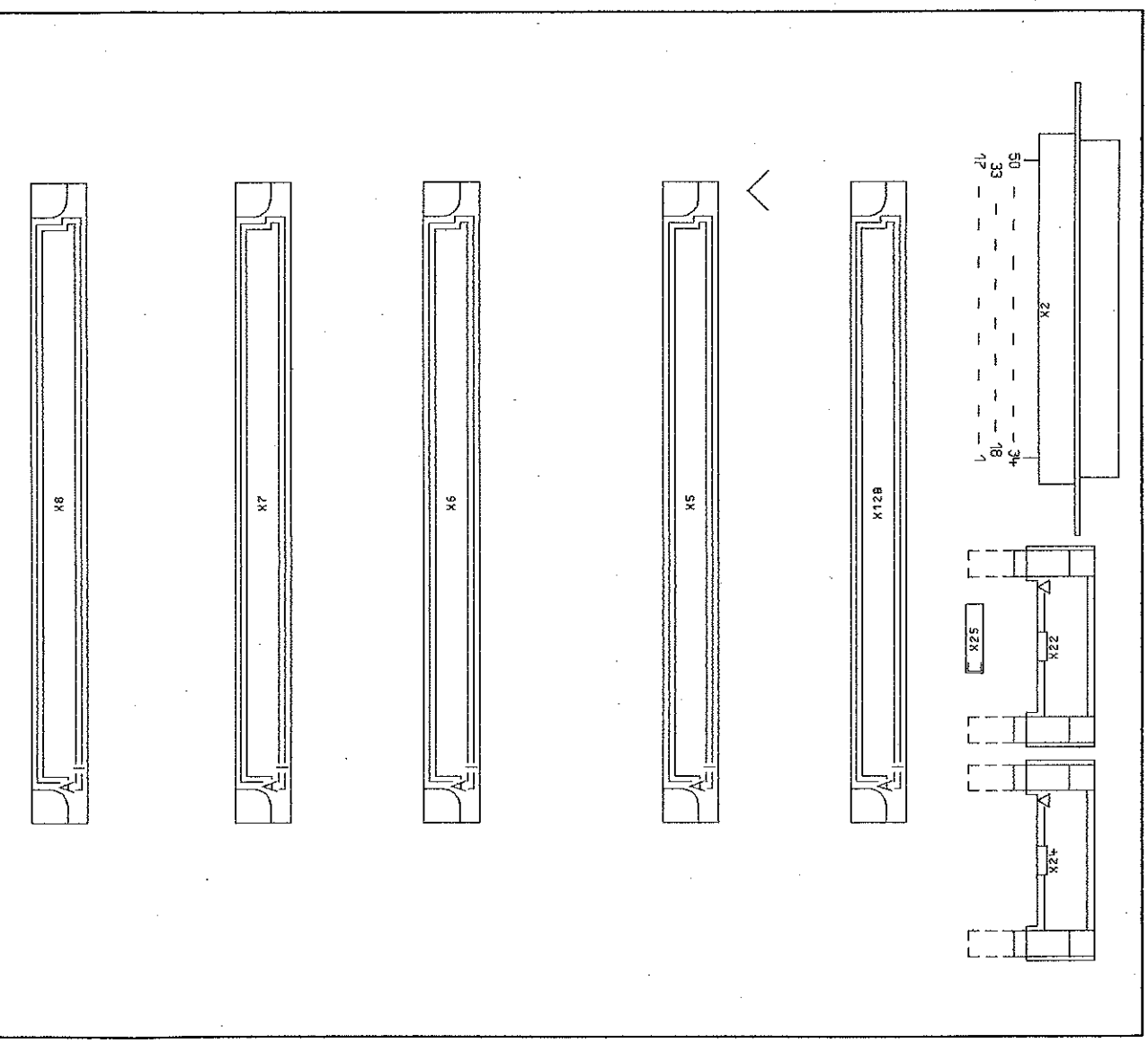
STROMLAUF GILT FUER VAR.02

CIRCUIT DIAGRAM IS VALID FOR MOD.02

ACHTU
ELEKTROST
BAUELEME
BESOH
ATTEN
ELECTROSTA
REQUIRE


A3
FRONTEINHEIT
FRONT ASSEMBLY
V24 TEST
* - NOT FITTED
A4
OPTION
A11
AUSGANGSTEIL 3GHZ
OUTPUT UNIT 3GHZ
A10
AUSGANGSTEIL 1,5GH
OUTPUT UNIT 1,5GH
VA15-N
VD5-P
VA7,5-P
VA15-P
VA24-P
X-AXIS
BURST TRIGGER
MARKER
BLANK
MODCTRL-IN
DIAG-15V
DIAG-5V
RES-P
SERBUS-INT
SERBUS-SYN
SERBUS-DAT
SERBUS-CLK
INT1
VDAM
GOUT
BLANK
AC-FAIL
FRONTEINHEIT
FRONT-ASSEMBLY
STROMLAUF GILT FUER VA
CIRCUIT DIAGRAM IS VALID FOR MOD
EITUNGSSTEUERUNG
UATOR CONTROL ASSEMBLY

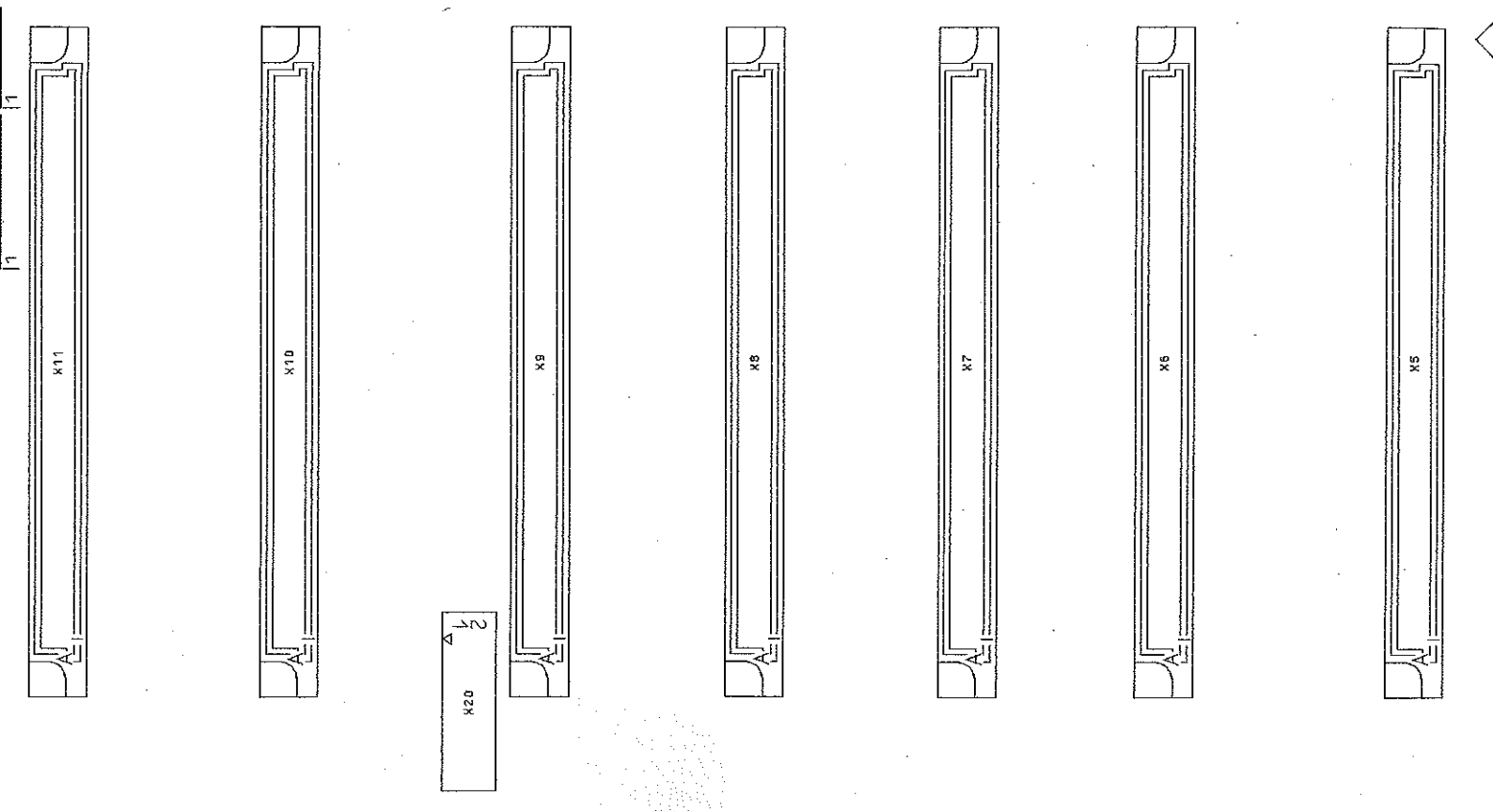

A3
FRONTEINHEIT
FRONT ASSEMBLY
V24 TEST
* - NOT FITTED
A4
OPTION
A
AUSGANGS
OUTPUT
VA15-N
VD5-P
VA7,5-P
VA15-P
VA24-P
X30 9 *
X30 7 *
X30 5 *
X30 3 *
X30 1 *
X27 6 *
X27 5 *
X27 4 *
X27 3 *
X27 2 *
X27 1 *
X50 50
X50 49
X50 48
X50 47
X50 46
X50 45
X-AXIS
X50 44
X50 43
X50 42
X50 41
BURST
TRIGGER
X50 40
X50 39
X50 38
X50 37
AC-FAIL
X50 36
X50 35
SYSRESET
X50 34
X50 33
MARKER
BLANK
X50 32
X50 31
MODCTRL-IN
X50 30
X50 29
VS12-P
X50 28
X50 27
X50 26
X50 25
STANDBY/ON
X50 24
X50 23
X50 22
X50 21
X50 20
X50 19
X50 18
X50 17
X50 16
X50 15
X50 14
X50 13
X50 12
X50 11
X50 10
X50 9
X50 8
X50 7
X50 6
X50 5
X50 4
X50 3
X50 2
X50 1
X4A 32
X4A 31
X4A 30
X4A 29
X4A 28
X4A 27
X4A 26
X4A 25
X4A 24
X4A 23
X4A 22
X4A 21
X4A 20 DIAG-15V
X4A 19 DIAG-5V
X4A 18 RES-P
X4A 17 SERBUS-INT
X4A 16 SERBUS-SYN
X4A 15 SERBUS-DAT
X4A 14
X4A 13
X4A 12 SERBUS-CLK
X4A 11
X4A 10
X4A 9
X4A 8
X4A 7 INT1
X4A 6
X4A 5
X4A 4
X4A 3
X4A 2
X4A 1
X15 16
X15 15
DIAG-5V
X15 14
X15 13
X15 12
X15 11
DIAG-5V
X15 10
X15 9
RES-P
X15 8
X15 7
SERBUS-INT
X15 6
X15 5
SERBUS-SYN
X15 4
X15 3
SERBUS-DAT
X15 2
X15 1
SERBUS-CLK
AC-FAIL
ZU EICHLEITUNGSSTEUERUNG
TO ATTENUATOR CONTROL ASSEMBLY
FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR
ZEICHN.-NR.
ST
CIR
1
2
3
4
A
B
C
D
E
F



150 200 250 300

01/01				1GPK	TAG	NAME	BENENNUNG
				BEARB.		JN	MOTHERBOARD
				GEPR.		JN	MOTHERBOARD
				NORM			Z
				PLOTT	26.01.94		
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1038.6760.01 ED
ÄEND. IND.	ÄENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERÄT SME			BLATT-NR. 17
				REF.1.V. 1038.6002			ERSTE Z.

7 8 9 10 11



BINDENDE ANGABEN UEBER VARIANTEN, TRIMMUERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SR.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

01/01			
BEND. IND.	BENDERUNGS-MITTEILUNG	DATUM	NAME



FUER DIESE ZEICHNUNG BEHALTEN WIR UNS ALLE RECHTE VOR.
DIESE ZEICHNUNG IST EIN RECHNERAUSDRUCK, RENDERUNGEN KOENNEN NUR DURCH RENDERN DES DATENSATZES ERFOLGEN

DARSTELLUNG SEITE B
VIEW ON SIDE B

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.